

Cct_No: 200
SBR IRS BSE #1 (Cont)

Plan + Existing

Trace 11

Function: IRS

Interface: VF 4W

Status: E , Active Circuit

Start of Circuit

Site	Mux	Port	TS	Sub	Card	Ch	Vertical	MDF Terminals	Point Status
DCC	DM2-5	i		-	VF E&M(2)	1	11C	41 - 44	E

First Part of standard Circuit

Site	Mux	Port	TS	Sub	Card	Ch			Point Status
DCC	DM2-5	1	6	-	DM2(1)				E
DCC	ADM-1	lf		E1	21ch E1(6)	1	B	1 - 2	E
DCC	ADM-1	1	8	E1	STM-1 O(1.1)				E
ADE	ADM-A	C9-2	8	E1	L1.2(9.2)				E
ADE	ADM-A	lf		E1	64x2M(10)	1			E
ADE	DB2-1	1	6	-	DB2(1)				E
ADE	DB2-1	2	6	-	DB2(1)				E
ADE	ADM-A	lf		E1	64x2M(10)	2			E
ADE	ADM-A	C8-1	8	E1	S1.1(8.1)				E
SBR	ADM-1	2	8	E1	STM-1 O(2.1)				E
SBR	ADM-1	lf		E1	21ch E1(7)	1	A	1 - 2	E
SBR	DM2-1	1	6	-	DM2(1)				E

Last Point on a Circuit

Site	Mux	Port	TS	Sub	Card	Ch	Vertical	MDF Terminals	Point Status
SBR	DM2-1	i		-	VF E&M(2)	1	5A	1 - 4	E

First Back-up path for a loop protected Cct. Parallel Type 1

Site	Mux	Port	TS	Sub	Card	Ch			Point Status
DCC	ADM-1	2	8	E1	STM-1 O(2.1)				E
SITE3	ADM-1	1	8	E1	STM-1 O(1.1)				PL
SITE3	ADM-1	2	8	E1	STM-1 O(2.1)				PL
HILL	ADM-1	1	8	E1	STM-1 O(1.1)				PL
HILL	ADM-1	2	8	E1	STM-1 O(2.1)				PL
WAV	ADM-1	1	8	E1	STM-1 O(1.1)				PL
WAV	ADM-1	2	8	E1	STM-1 O(2.1)				PL
BRI	ADM-1	1	8	E1	STM-1 O(1.1)				E
BRI	ADM-1	2	8	E1	STM-1 O(2.1)				E
SBR	ADM-1	1	8	E1	STM-1 O(1.1)				E
SBR	ADM-1	2	10	E1	STM-1 O(2.1)				E
ADE	ADM-A	C8-1	10	E1	S1.1(8.1)				E